

### **Towards Petaflop simulations of core collapse supernovae**

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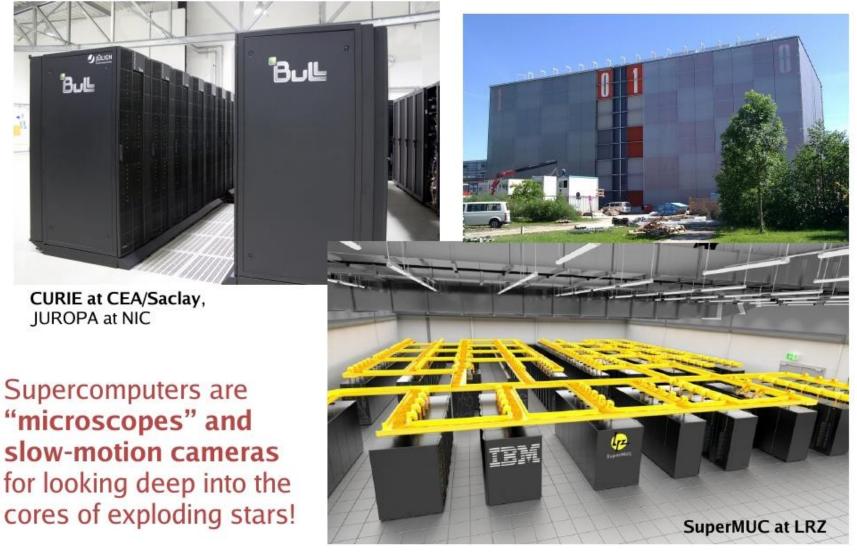


### Content

- > Supernova modelling: the challenges
- > The VERTEX code
- > Scaling
- > Performance
- Conclusions

### How to understand them...

# Theorists' Tools for Verification



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# Supernova modeling

# Support by Computer Time Grants through:



# Support by Research Grants from:





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### The challenges: The curse of dimensions

Boltzmann equation determines the neutrino distribution function

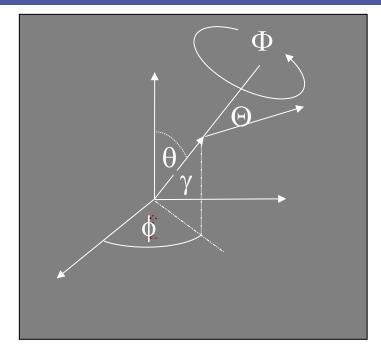
 $f(r,\theta,\phi,\Theta,\Phi,\epsilon,t)$ 

Integration in momentum space: source terms for hydrodynamics

 $Q(r,\theta,\phi,t), dY_e(r,\theta,\phi,t)/dt$ 

### Solution approach

- **3D** hydro + **6D** direct discretization of Boltzmann Eq. (code development by Sumiyoshi & Yamada 2012)
- **3D** hydro + two-moment closure of Boltzmann Eq. (next feasible step to full 3D; cf. Kuroda et al. 2012)
- 2D or 3D hydro + "ray-by-ray-plus" variable
   Eddington factor method (MPA, RZG)



**Required resources** 

- >= 10 100 PFlop/s (sustained!)
- >= 1 10 PFlop/s
- >= 0.1 TFlop/s 1 TFlop/s (2D)
- >= 0.1 Pflop/s 1 PFlop/s (3D)

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### 2D and 3D supernova modeling

Time dependent simulations:  $t \sim 1$  second,  $\sim 10^6$  timesteps

**CPU-time requirements for one model run** 

In 2D with 600 radial zones, 1 degree lateral resolution
 3 \*10<sup>18</sup> Flops, about 10<sup>6</sup> processor-core hours

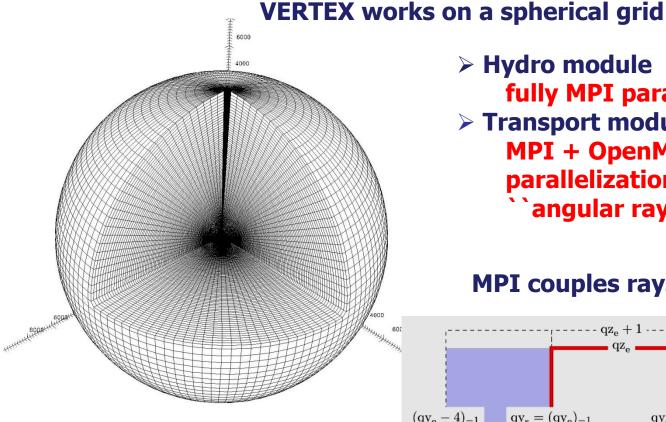
In 3D with 600 radial zones, 1.5 degree angular resolution
 3 \*10<sup>20</sup> Flops, about 10<sup>8</sup> processor-core hours

# and in the near future with increased resultion this will be at least 3 times more

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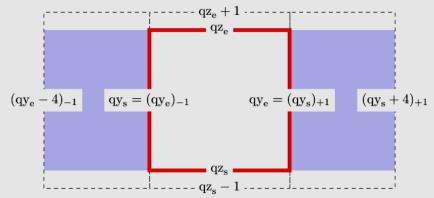
### The VERTEX code : Setup

### Hybrid MPI/OpenMP parallelized version of VERTEX



> Hydro module fully MPI parallelized > Transport module **MPI + OpenMP coarse grain** parallelization along angular rays''

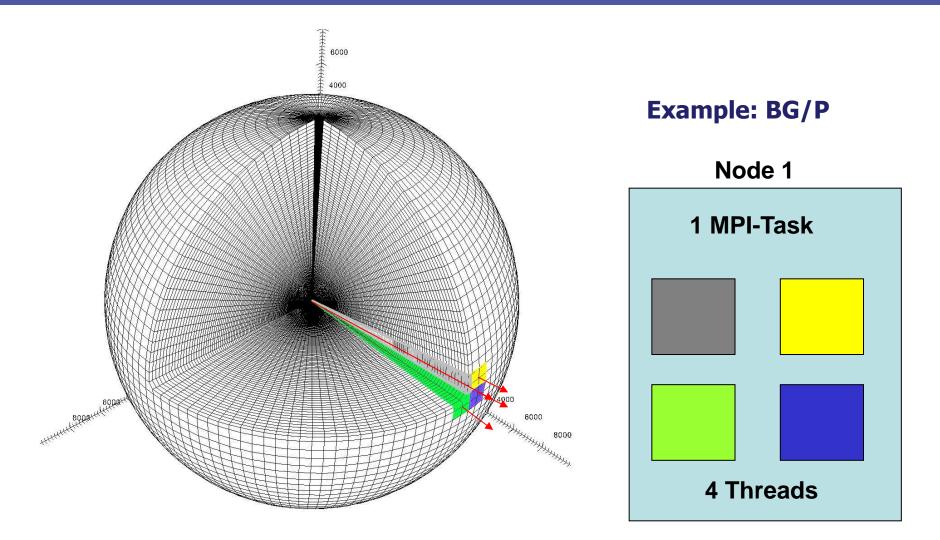
### **MPI couples rays**



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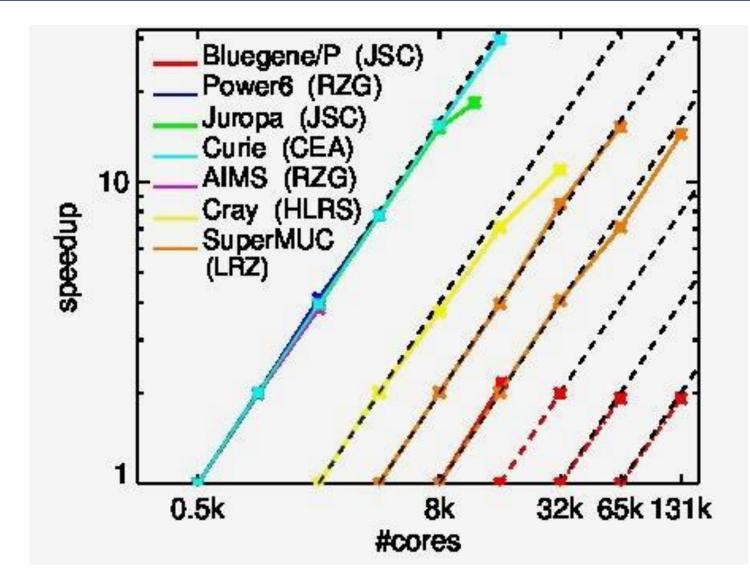
### Mapping onto the processor grid



in the node: 1 angular ray per Core

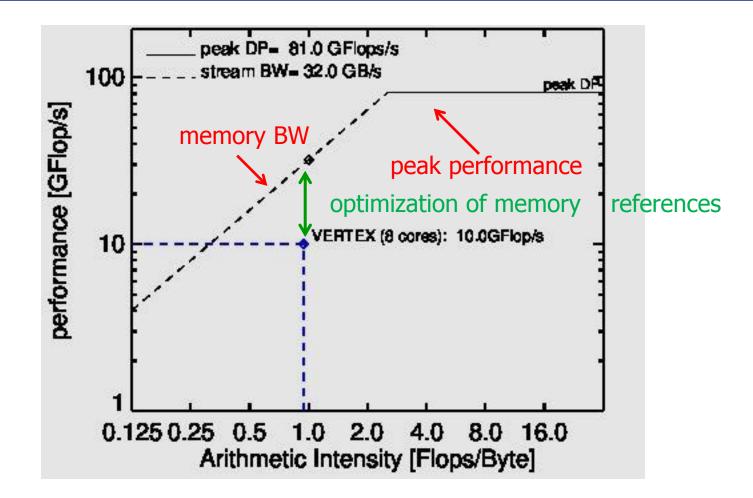
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### Scaling



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### **Performance Estimate: Roofline model**



Measurement: Intel Nehalem node, 8 cores @ 2.53 GHz, SSE4.2 done with the ``likwid´´,``perflib´´, and ``PAPI´´ => VERTEX is ``memory bound´´ runs ~ 12.5% of peak performance

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### **Performance: SandyBridge cores**

### **Performance on 131k cores of SuperMuc:**

- > 8192 nodes
  - => ~ 0.245 PFlop/s on LRZ`s SuperMUC
  - ~ 9 % of peak performance

# Scaling: development

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> in the future data movement will be the problem for good scaling

- problem of MPI at large scales ?
- memory per cores will still decrease
- data transfer within a node (scaling within a "traditional node")
- OpenMP scaling (e.g. Xeon PHI)
- data transfer to accelerator cards (Xeon PHI, GPU)
- => think of (and rewrite) memory layout that it scales on many core nodes as well as 10000 -100000 s of nodes
- > Other issue: robustness of machines

### **Even stronger scaling capability needed:**

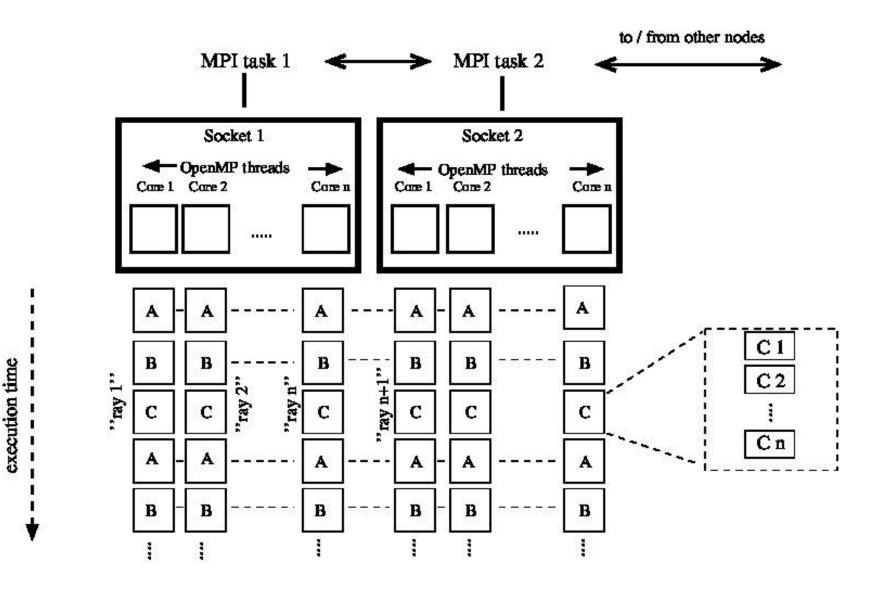
- > The next or next-next supernova models will need even more computing power!
- **``Traditional´´ development: usage of more cores** 
  - > at the moment another third parallelization level is implemented
    - => within one ``ray´´ a nested OpenMP workload is possible
    - => use 10 to 20 times more cores

**Accelerator cards development:** 

the usage of GPUs and Xeon Phi is tested
 => GPU implementation already shows promising results
 => factor 2 achieved, 3 seems possible

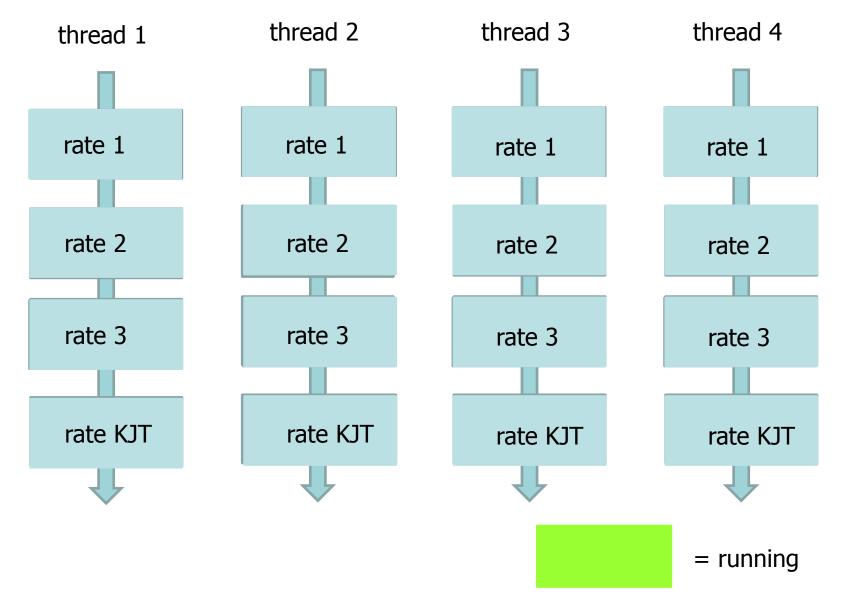
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### **Current Code improvements**



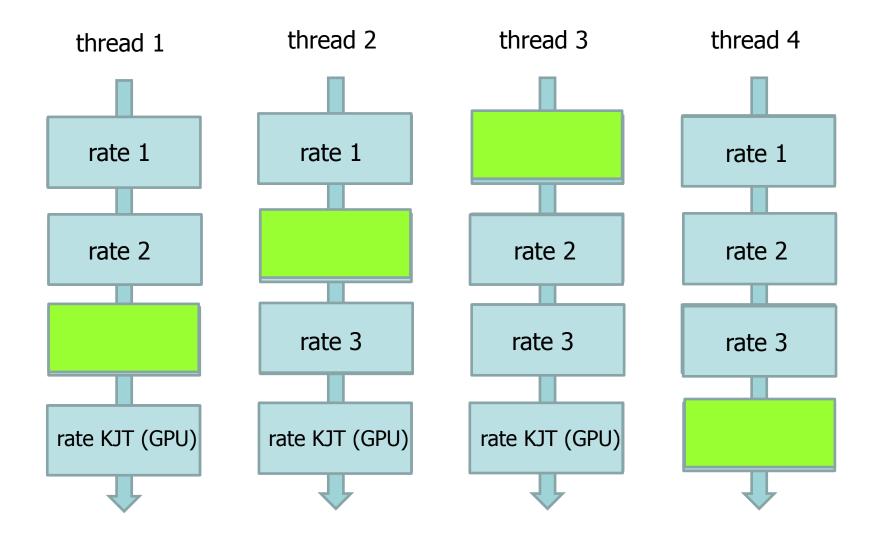
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# Load-Balancing (Host only)



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# Load-Balancing (HOST + GPU)



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### Summary

- We have demonstrated the need of supernova research for high end HPC computing
- > at the moment production runs are done on 16k -32k cores, the next generation of models will need about 65k – 100k cores
- However, we have demonstrated that VERTEX scales already now on 131k cores of LRZ`s SuperMuc (and on Bluegene/P)
- VERTEX operates with ~ 10% of the peak performance on Intel Sandybridge nodes
- > The next generation of VERTEX is already under development
  - nested OpenMP level: about 1 to 2 Mcores might be used
  - usage of accelerator cards:
    - \* GPU acceleration: speedup of 2 achieved 3 possible
    - \* Xeon PHI: still under investigation

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**Questions ?**